

## Schematics

### Top level

QM\_SOLO\_LFR-01.08-TOP.jpg

### Power supply

QM\_SOLO\_LFR-01.08-PWR.jpg

### Input buffer

QM\_SOLO\_LFR-01.08-BUFF.jpg

QM\_SOLO\_LFR-01.08-AN.jpg

### Bias fail multiplexers

QM\_SOLO\_LFR-01.08-BFAIL.jpg

### ADCs

QM\_SOLO\_LFR-01.08-ADC.jpg

### Search Coil calibration

QM\_SOLO\_LFR-01.08-CAL.jpg

### Housekeepings

QM\_SOLO\_LFR-01.08-HK.jpg

### FPGA

QM\_SOLO\_LFR-01.08-FPGA.jpg

### SRAM

QM\_SOLO\_LFR-01.08-SRAM.jpg

### Spacewire

QM\_SOLO\_LFR-01.08-SPW.jpg

### Changelog

QM\_SOLO\_LFR-01.08-Changelog.jpg

### Files

QM_SOLO_LFR-01.08-AN.jpg	1.35 MB	16/10/2014	Alexis Jeandet
QM_SOLO_LFR-01.08-ADC.jpg	2.55 MB	16/10/2014	Alexis Jeandet
QM_SOLO_LFR-01.08-BFAIL.jpg	1.42 MB	16/10/2014	Alexis Jeandet
QM_SOLO_LFR-01.08-CAL.jpg	2.18 MB	16/10/2014	Alexis Jeandet
QM_SOLO_LFR-01.08-BUFF.jpg	1.95 MB	16/10/2014	Alexis Jeandet
QM_SOLO_LFR-01.08-Changelog.jpg	2.13 MB	16/10/2014	Alexis Jeandet
QM_SOLO_LFR-01.08-HK.jpg	1.83 MB	16/10/2014	Alexis Jeandet
QM_SOLO_LFR-01.08-FPGA.jpg	2.86 MB	16/10/2014	Alexis Jeandet
QM_SOLO_LFR-01.08-SPW.jpg	1.44 MB	16/10/2014	Alexis Jeandet
QM_SOLO_LFR-01.08-PWR.jpg	2.32 MB	16/10/2014	Alexis Jeandet
QM_SOLO_LFR-01.08-TOP.jpg	2.89 MB	16/10/2014	Alexis Jeandet
QM_SOLO_LFR-01.08-SRAM.jpg	2.11 MB	16/10/2014	Alexis Jeandet