

Schematics

Top level

QM_SOLO_LFR-01.08-TOP.jpg

Power supply

QM_SOLO_LFR-01.08-PWR.jpg

Input buffer

QM_SOLO_LFR-01.08-BUFF.jpg

QM_SOLO_LFR-01.08-AN.jpg

Bias fail multiplexers

QM_SOLO_LFR-01.08-BFAIL.jpg

ADCs

QM_SOLO_LFR-01.08-ADC.jpg

Search Coil calibration

QM_SOLO_LFR-01.08-CAL.jpg

Housekeepings

QM_SOLO_LFR-01.08-HK.jpg

FPGA

QM_SOLO_LFR-01.08-FPGA.jpg

SRAM

QM_SOLO_LFR-01.08-SRAM.jpg

Spacewire

QM_SOLO_LFR-01.08-SPW.jpg

Changelog

QM_SOLO_LFR-01.08-Changelog.jpg

Files

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QM_SOLO_LFR-01.08-ADC.jpg	2.55 MB	16/10/2014	Alexis Jeandet
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QM_SOLO_LFR-01.08-HK.jpg	1.83 MB	16/10/2014	Alexis Jeandet
QM_SOLO_LFR-01.08-FPGA.jpg	2.86 MB	16/10/2014	Alexis Jeandet
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