

Solar Orbiter LFR - Issues

#	Tracker	Status	Priority	Subject	Assignee	Updated
555	Task	Closed	Normal	Errata RPW-MEB-LFR-SPC-00061-1-8_FPGA_Architecture_Design	Jean-Christophe Pellion	26/10/2015 04:20 PM
403	Task	Rejected	Low	Evaluate some opensource VHDL IPs	Alexis Jeandet	28/07/2015 05:35 PM
361	Task	Closed	Normal	Design DiscoSpace	paul leroy	09/06/2015 10:48 AM
239	Task	Closed	Normal	Parametre R2 du Waveform Picker - Doc and Verif	paul leroy	10/02/2015 09:05 AM
233	Task	Closed	Normal	retrofit FPGA sur EM+	Alexis Jeandet	28/07/2015 05:38 PM
230	Task	Closed	Normal	Rédaction de la demande de modification pour STEEL	Vincent Leray	08/04/2015 07:47 AM
227	Task	Closed	Normal	Calibration	Jean-Christophe Pellion	10/02/2015 04:22 PM
19	Task	Closed	Normal	Sélection du sous-traitant pour les réalisation QM/FM/SM et boitiers	paul leroy	10/02/2015 03:20 PM
17	Task	Closed	Normal	Finalisation des développements du module Spectral Matrices	Martin Morlot	27/08/2014 10:27 AM
14	Task	Closed	High	Fichier de configuration FPGA	Jean-Christophe Pellion	13/03/2014 07:17 AM
13	Task	Closed	Normal	Mise à jour de la documentation VHDL	Jean-Christophe Pellion	13/03/2014 07:17 AM