

## Solar Orbiter LFR - Issues

#	Tracker	Status	Priority	Subject	Assignee	Updated
555	Task	Closed	Normal	Errata RPW-MEB-LFR-SPC-00061-1-8_FPGA_Architecture_Design	Jean-Christophe Pellion	26/10/2015 04:20 PM
403	Task	Rejected	Low	Evaluate some opensource VHDL IPs	Alexis Jeandet	28/07/2015 05:35 PM
361	Task	Closed	Normal	Design DiscoSpace	paul leroy	09/06/2015 10:48 AM
258	Support	Closed	Normal	Numéro de version du code VHDL	Jean-Christophe Pellion	10/02/2015 04:27 PM
125	Bug	Closed	High	LFR ADC data justification	paul leroy	10/02/2015 09:03 AM