

LFR-FSW - Bug #408

R3 *** TM_LFR_SCIENCE_NORMAL_SWF_Fi : Bad sub-type

06/05/2015 11:26 AM - Veronique bouzid

Status:	Closed	Start date:	06/05/2015
Priority:	Normal	Due date:	
Assignee:	Veronique bouzid	% Done:	0%
Category:		Estimated time:	0.00 hour
Target version:		Spent time:	0.00 hour
revision:	r0		

Description

Les paquets TM_LFR_SCIENCE_NORMAL_SWF_Fi sont impactés par les modifications de la R3.
EN effet, il y a maintenant une difference entre
- le sub-type d'un paquet TM_LFR_SCIENCE de taille fixe --> sub-type = 3
- le sub-type d'un paquet TM_LFR_SCIENCE de taille variable --> sub-type = 6

Les TM_LFR_SCIENCE_NORMAL_SWFi sont des paquets de taille variable, donc sub-type doit etre 6.

Le script utilisé se trouve dans /home/validation/SCRIPT/R3/just_normal_mode.py.
Le fichier de log utilisé est /home/validation/2015_05_06-10_01_18-Detail.txt

10:00:10.179089, /\(tm unknown): **PID=76 CAT=12 TYPE=21 SUB-TYPE=3 Length(ccsds)=3678 SID=3**, TIME=0x80000137c0cd
10:00:10.196405, /\(tm unknown): PID=76 CAT=12 TYPE=21 SUB-TYPE=3 Length(ccsds)=3678 SID=3, TIME=0x80000137c3f7
10:00:10.210724, /\(tm unknown): PID=76 CAT=12 TYPE=21 SUB-TYPE=3 Length(ccsds)=3678 SID=3, TIME=0x80000137c722
10:00:10.223266, /\(tm unknown): PID=76 CAT=12 TYPE=21 SUB-TYPE=3 Length(ccsds)=3678 SID=3, TIME=0x80000137ca4d
10:00:10.228667, /\(tm unknown): PID=76 CAT=12 TYPE=21 SUB-TYPE=3 Length(ccsds)=3678 SID=3, TIME=0x80000137cd77
etc....

Contexte de test

FSW 3.0.0.0
VHDL 0..1.68
Mini-lfr II
SocExplorer 0.6.0
StarDundee spacewire

History

#1 - 18/05/2015 10:43 AM - paul leroy

- Status changed from New to Resolved

- Assignee changed from paul leroy to Veronique bouzid

corrected in version >= 3.0.0.1

#2 - 05/06/2015 04:00 PM - Veronique bouzid

- Status changed from Resolved to Closed

Le bug est corrigé.

Le script /home/validation/SCRIPT/R3/just_normal_mode.py a été relancé dans le dernier environnement de test opérationnel.

15:06:26.41437, TM_LFR_SCIENCE_NORMAL_SWF_F0, CCSDS_VERSION_NUMBER = 0, PACKET_TYPE: TM_PACKET = 0,
DATA_FIELD_HEADER_FLAG: WITH_HEADER = 1, PROCESS_ID: RPW_PID_2 = 76, PACKET_CATEGORY:
PRIVATE_SCIENCE_OR_TELECOMMAND = 12, (PACKET_ID=0xccc), SEGMENTATION_GROUPING_FLAG: STANDALONE_PACKET = 3,
SEQUENCE_CNT=961, (PACKET_SEQUENCE_CONTROL=0xc3c1), PACKET_LENGTH=3671, SPARE_1=0, PUS_VERSION = 1, SPARE_2=0,
SERVICE_TYPE: SCIENCE_DATA_TRANSFER = 21, **SERVICE_SUBTYPE: SCIENCE_REPORT_VARIABLE_LENGTH = 6**, DESTINATION_ID:
GROUND = 0, TIME=0x8000012ed313, PA_LFR_SID_PKT: SC_N_SWF_F0 = 3, PA_BIA_MODE_MUX_SET: SET_0 = 0,
PA_BIA_MODE_HV_ENABLED: ENABLED = 1, PA_BIA_MODE_BIAS1_ENABLED: ENABLED = 1, PA_BIA_MODE_BIAS2_ENABLED: ENABLED
= 1, PA_BIA_MODE_BIAS3_ENABLED: ENABLED = 1, PA_BIA_ON_OFF: OFF = 0, SPARE=0x0, SY_LFR_BW=1, SY_LFR_SP0=0,
SY_LFR_SP1=0, SY_LFR_R0=0, SY_LFR_R1=0, SY_LFR_R2=0, PA_LFR_PKT_CNT=7, PA_LFR_PKT_NR=1,
PA_LFR_ACQUISITION_TIME=0x8000012ed313, PA_LFR_SWF_BLK_NR=304

les fichiers de log (2015_06_05-15_13_50*) sont dans le répertoire /home/validation/data/R3

Contexte de test

FSW 3.0.0.3

VHDL 1.1.83

EM1 sans timegen

SocExplorerEngine.getSocExplorer: Version = 0.6.2, Branch = default, Changeset = 819d0376d481

Stardundee