## Solar Orbiter LFR - Task #403

## **Evaluate some opensource VHDL IPs**

05/05/2015 02:58 PM - Alexis Jeandet

Status: Rejected Start date: 05/05/2015

Priority: Low Due date:

Assignee: Alexis Jeandet % Done: 0%

Category: Estimated time: 0.00 hour

Target version: Spent time: 0.00 hour revision:

Description

We could evaluate the possibility to evaluate some open-source FFT IPs.

here is an example of FFT, with some documentation.

This IP is mainly an 32 bits and 8 points FFT which could be modified to 16 bits and 256 points.

## History

## #1 - 28/07/2015 05:35 PM - Alexis Jeandet

- Status changed from New to Rejected

Not possible due to a lack of time.

20/03/2024 1/1