

# Solar Orbiter LFR - Bug #125

## LFR ADC data justification

17/04/2014 02:24 PM - Alexis Jeandet

<b>Status:</b>	Closed	<b>Start date:</b>	17/04/2014
<b>Priority:</b>	High	<b>Due date:</b>	
<b>Assignee:</b>	paul leroy	<b>% Done:</b>	0%
<b>Category:</b>		<b>Estimated time:</b>	0.00 hour
<b>Target version:</b>		<b>Spent time:</b>	0.00 hour
<b>revision:</b>	r342		
<b>Description</b>			
It seems that LFR ADC data are right justified, normally it should be right justified. When we inject 1Vpp in LFR the data read is 4540pp instead of 1*6/65536. It seems to be due to the 14 to 16 bit conversion.			

### History

#### #1 - 28/04/2014 04:13 PM - Jean-Christophe Pellion

- Status changed from New to Resolved
- Assignee changed from Jean-Christophe Pellion to paul leroy

#### LFR-EM - 0.1.11

- INFOS  
Update Sample transformation between ADC and iir\_filter

For ALL\_SAMPLE :

```
SAMPLE_IIR(15 downto 0) <= SAMPLE_ADC(13 downto 0) & '0' & '0';
```

- PDB  
[https://hephaistos.lpp.polytechnique.fr/redmine/attachments/download/261/LFR-em-WFP\\_MS-0.1.11.pdb](https://hephaistos.lpp.polytechnique.fr/redmine/attachments/download/261/LFR-em-WFP_MS-0.1.11.pdb)  
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- Revision  
r353  
[https://hephaistos.lpp.polytechnique.fr/rhocode/HG\\_REPOSITORIES/LPP/INSTRUMENTATION/VHD\\_Lib/changeset/883154c0e96fc25b6db6ff74e82d5c6c6265d472](https://hephaistos.lpp.polytechnique.fr/rhocode/HG_REPOSITORIES/LPP/INSTRUMENTATION/VHD_Lib/changeset/883154c0e96fc25b6db6ff74e82d5c6c6265d472)  
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#### #2 - 10/02/2015 09:03 AM - paul leroy

- Status changed from Resolved to Closed